

# DECLARATION FOR PATENT APPLICATION AND POWER OF ATTORNEY

As a below-named inventor, I hereby declare that:

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I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled **JFET/CMOS IMAGER**, the specification of which

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(Provisional Application)	(Filing Date)	(Status)

I hereby appoint the following attorneys and/or agents to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

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Gloria L. Norberg,	Reg. No. 36,706

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Shayne X. Short,	Reg. No. P-45,105
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Of:

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Daniel N. Yannuzzi, Reg. No. 36,727  
Semion Talpalatsky, Reg. No. 35,380

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**First named inventor:**

Full name Lester J. Kozlowski  
 Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_  
 Residence Simi Valley, CA Citizenship: USA  
 Post Office Address 212 Golden Fern Court, Simi Valley, CA 93065

**Second named inventor:**

Full name Frank Chang  
 Inventor's Signature *MFC* Date 4/9/00  
 Residence ~~Thousand Oaks, CA~~ Los Angeles, CA Citizenship: USA  
 Post Office Address ~~3122 Montagne Way, Thousand Oaks, CA 91362~~  
10112 Hollow Glen Circle, Los Angeles, CA 90077

**Third named inventor:**

Full name Wu-Jing Ho  
 Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_  
 Residence Cupertino, CA Citizenship: USA  
 Post Office Address 10126 Carmen Road, Cupertino, CA 95014

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Full name Wu-Jing Ho  
 Inventor's Signature Wu-Jing Ho Date 3/18/2000  
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